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on interacting diode junctions and is applicable to all the transistor operating modes Option (d) Two Marks Questions 1. In a transistor having finite  $\beta$ , the forward bias across the base emitter junction is kept constant and the reverse bias across the collector base junction is increased. Neglecting the leakage across the collector base

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biasing is the process of maintaining the transistor in active or linear region so that it can ready to amplify the small signal applied. the best biasing circuit for BJT is voltage divider bias ...

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4. Bipolar Junction Transistors TLT-8016 Basic Analog Circuits 2005/2007 3 Basic: Operation in the Active region Figure 4.2 An npn transistor with variable biasing sources (common-emitter configuration). Shokley equation for the emitter current

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In the common emitter amplifier shown, the transistor has a forward current gain of 100, and a base to emitter voltage of 0.6 volts. Assume  $I_{CO}$  to be negligible. Choose value for  $R_1$  and  $R_3$  such that the transistor has a collector current of 1 mA and a collector to emitter voltage of 2.5 volts.

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This is the Multiple Choice Questions in Chapter 8: Transistor Biasing from the book Electronic Principles 7th Edition by Albert Malvino.If you are looking for a reviewer in Electronics Engineering this will definitely help.

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