

## Silicon Processing For The Vlsi Era Process Technology

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Silicon Processing For The VLSI Era is a 3 volume treatise which provides a comprehensive, up-to-date treatment of this technology. Volume 1 covers the details of individual process steps used in fabricating silicon ICs. Volume 2 describes how these process steps are combined to make VLSI and ULSI structures.

Silicon Processing For The Vlsi Era by Stanley Wolf

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Silicon Processing for the VLSI Era, Vol. 1: Process ...

This book collects the hot issues on the very recent deep submicron (less than 0.18um) semiconductor process technology, such as EUV lithography, high and low k materials, CMP, and 300mm wafer. It seems to provide a good guideline to anyone who wonders what it is going on...

Silicon processing for the VLSI era (Book, 1986) [WorldCat ...

Very-large-scale integration ( VLSI) is the process of creating an integrated circuit (IC) by combining millions of MOS transistors onto a single chip. VLSI began in the 1970s when MOS integrated circuit chips were widely adopted, enabling complex semiconductor and telecommunication technologies...

Silicon Processing For The VLSI Era Vol. 2 Process ...

vii SILICON PROCESSING FOR THE VLSI ERA Vol. 4 [[ Deep-Submicron Process Technology DETAILED TABLE OF CONTENTS PREFACE Chap. 1 - THE EVOLUTION OF THE STRUCTURE OF MOSFETS 1 1.1 THE STRUCTURE OF DEEP-SUBMICRON MOSFETS: (0.25- $\mu$ m to 0.13- $\mu$ m) - COMPARED TO THE STRUCTURE OF [[CONVENTIONAL]] MOSFETS (2.0- $\mu$ m to 0.5- $\mu$ m) 2 1.1.1 Evolution of the MOSFET Gate Stack and Contact Structure 1.1.2 Gate Dielectric Materials in Deep-Submicron MOSFETS 1.1.3 Doping-Concentration Profiles of the MOSFET ...

Lattice Press

Etching a (100) silicon surface through a rectangular hole in a masking material, for example a hole in a layer of silicon nitride, creates a pit with flat sloping {111}-oriented sidewalls and a flat (100)-oriented bottom. The {111}-oriented sidewalls have an angle to the surface...

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"Silicon Processing for the VLSI Era" is by far the most authoritative guide to semiconductor processing and nano-fabrication in existence. Practical processing techniques as well as their underlying fundamental principles are thoroughly covered in detail.

Semiconductor device fabrication - Wikipedia

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Etching (microfabrication) - Wikipedia

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